



PTO/SB/08A (10-01)

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# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 of 1

## Complete if known

Application Number	10/672,961
Filing Date	September 26, 2003
First Named Inventor	Glenn J. Leedy
Art Unit	2822
Examiner Name	Monica Lewis
Attorney Docket Number	ELM-2 Cont. 4

## U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Documents	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code <sup>2</sup> (if known)			
/TD/		4,954,875	09-04-90	Clements	
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## FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Documents	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	1*
		Country Code <sup>3</sup> - Number <sup>2</sup> - Kind Code <sup>4</sup>				
/TD/		EP 0 238 089 A2	09-23-87	Fujitsu Ltd.		
/TD/		EP 0 703 618 A1	03-27-96	FRAUNHOFER- GESELLSCHAFT ZUR FÖRDERUNG DER ANGEWANDTEN FORSCHUNG E.V.		
/TD/		EP 0 703 619 A1	03-27-96	FRAUNHOFER- GESELLSCHAFT ZUR FÖRDERUNG DER ANGEWANDTEN FORSCHUNG E.V.		

## NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	1*
/TD/		Mitsumasa Koyanagi et al., "Design of 4-KBIT X 4-layer Optically Coupled Three-Dimensional Common Memory For Parallel Processor System," IEEE Journal of Solid-State Circuits, Vol. 25, No. 1, February 1, 1990.	

/Thomas Dickey/

03/17/2010